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on
4-3-02**In the United States Patent and Trademark Office**Serial number 09/340,172Application filed June 25, 1999Applicant Derek WongApplication Title Methods for Increasing Instruction-Level Parallelism in Microprocessors and Digital SystemsExaminer/GAU Eric Coleman / 2183Mailed March 20, 2002**Petition for Extension of Time (Rules 136 and 17(a)-(d))**Outstanding Office Action mailed September 20, 2001Original period for response expired December 20, 2001Request for extension of 3 months to March 20, 2002Small Entity Fee enclosed \$445**RECEIVED**

APR 01 2002

Technology Center 2100

To:

Assistant Commissioner for Patents
United States Patent and Trademark Office
Washington, DC 20231

Sir:

In the above application, applicant respectfully petitions that the period for response to the outstanding Office Action indicated above be extended for the additional months indicated above. A response to such Office Action and the above Petition fee (Small Entity) are enclosed herewith.

Very Respectfully,

Applicant Derek Wong
Telephone 408-927-7940

03/28/2002 DTESSEM1 00000074 09340172

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445.00 OP

Express Mail Label # EU183174322US Date of Deposit March 20, 2002

I hereby certify that this paper or fee is being deposited with the United States Postal Service using "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and is addressed to "Assistant Commissioner for Patents, Washington, DC 20231."

Signed

Inventor